

# Compiler course

Chapter 6

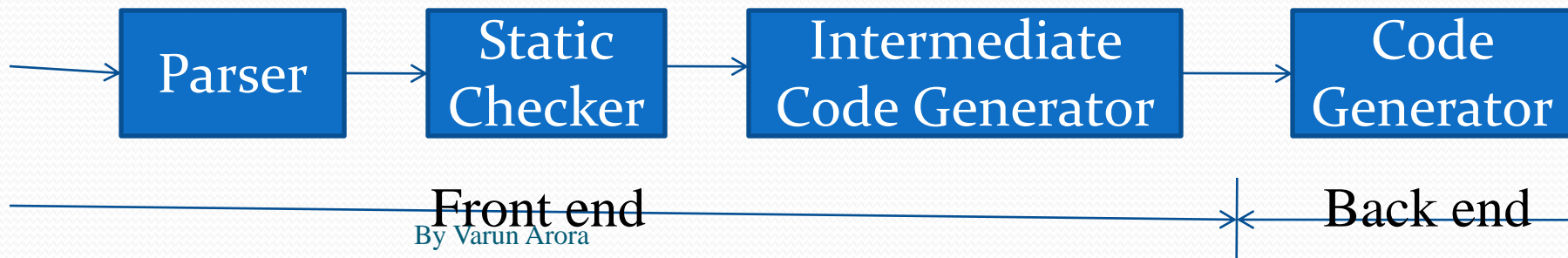
Intermediate Code Generation

# Outline

- Variants of Syntax Trees
- Three-address code
- Types and declarations
- Translation of expressions
- Type checking
- Control flow
- Backpatching

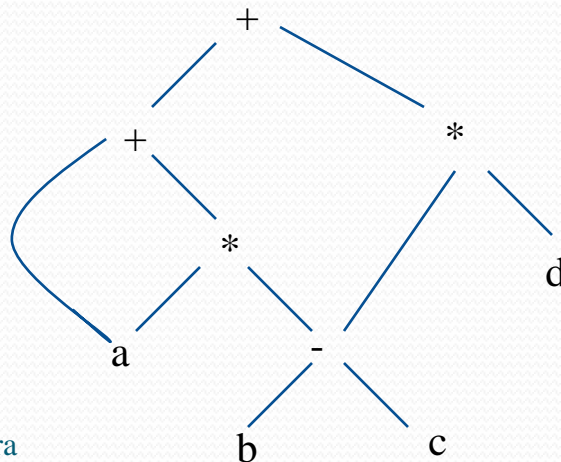
# Introduction

- Intermediate code is the interface between front end and back end in a compiler
- Ideally the details of source language are confined to the front end and the details of target machines to the back end (a m\*n model)
- In this chapter we study intermediate representations, static type checking and intermediate code generation



# Variants of syntax trees

- It is sometimes beneficial to create a DAG instead of a tree for Expressions.
- This way we can easily show the common sub-expressions and then use that knowledge during code generation
- Example:  $a + a * (b - c) + (b - c) * d$



# SDD for creating DAG's

## Production

- 1)  $E \rightarrow E1+T$
- 2)  $E \rightarrow E1-T$
- 3)  $E \rightarrow T$
- 4)  $T \rightarrow (E)$
- 5)  $T \rightarrow id$
- 6)  $T \rightarrow num$

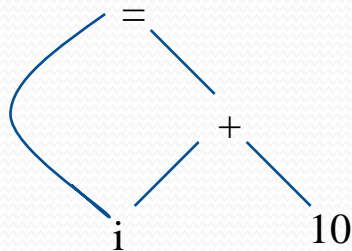
## Semantic Rules

- $E.node = \text{new Node}('+', E1.node, T.node)$   
 $E.node = \text{new Node}('-', E1.node, T.node)$   
 $E.node = T.node$   
 $T.node = E.node$   
 $T.node = \text{new Leaf}(id, id.entry)$   
 $T.node = \text{new Leaf}(num, num.val)$

## Example:

- 1)  $p1 = \text{Leaf}(id, \text{entry-a})$
- 2)  $p2 = \text{Leaf}(id, \text{entry-a}) = p1$
- 3)  $p3 = \text{Leaf}(id, \text{entry-b})$
- 4)  $p4 = \text{Leaf}(id, \text{entry-c})$
- 5)  $p5 = \text{Node}('-', p3, p4)$
- 6)  $p6 = \text{Node}('*', p1, p5)$
- 7)  $p7 = \text{Node}('+', p1, p6)$
- 8)  $p8 = \text{Leaf}(id, \text{entry-b}) = p3$
- 9)  $p9 = \text{Leaf}(id, \text{entry-c}) = p4$
- 10)  $p10 = \text{Node}('-', p3, p4) = p5$
- 11)  $p11 = \text{Leaf}(id, \text{entry-d})$
- 12)  $p12 = \text{Node}('*', p5, p11)$
- 13)  $p13 = \text{Node}('+', p7, p12)$

# Value-number method for constructing DAG's

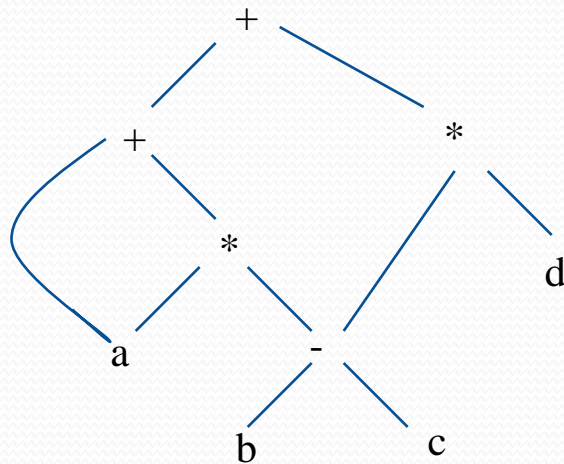


id	→ To entry for i	
num	10	
+	1	2
3	1	3

- Algorithm
  - Search the array for a node M with label op, left child l and right child r
  - If there is such a node, return the value number M
  - If not create in the array a new node N with label op, left child l, and right child r and return its value
- We may use a hash table

# Three address code

- In a three address code there is at most one operator at the right side of an instruction
- Example:



$t1 = b - c$   
 $t2 = a * t1$   
 $t3 = a + t2$   
 $t4 = t1 * d$   
 $t5 = t3 + t4$

# Forms of three address instructions

- $x = y \text{ op } z$
- $x = \text{op } y$
- $x = y$
- goto L
- if x goto L and ifFalse x goto L
- if x relop y goto L
- Procedure calls using:
  - param x
  - call p,n
  - $y = \text{call } p,n$
- $x = y[i]$  and  $x[i] = y$
- $x = \&y$  and  $x = *y$  and  $*x = y$



# Example

- do  $i = i+1$ ; while ( $a[i] < v$ );

```
L:    t1 = i + 1
      i = t1
      t2 = i * 8
      t3 = a[t2]
      if t3 < v goto L
```

Symbolic labels

```
100:  t1 = i + 1
101:  i = t1
102:  t2 = i * 8
103:  t3 = a[t2]
104:  if t3 < v goto 100
```

Position numbers

# Data structures for three address codes

- Quadruples
  - Has four fields: op, arg1, arg2 and result
- Triples
  - Temporaries are not used and instead references to instructions are made
- Indirect triples
  - In addition to triples we use a list of pointers to triples

# Example

- $b * \text{minus } c + b * \text{minus } c$

## Three address code

$t1 = \text{minus } c$   
 $t2 = b * t1$   
 $t3 = \text{minus } c$   
 $t4 = b * t3$   
 $t5 = t2 + t4$   
 $a = t5$

## Quadruples

op	arg1	arg2	result
minus	c		t1
*	b	t1	t2
minus	c		t3
*	b	t3	t4
+	t2	t4	t5
=	t5		a

## Triples

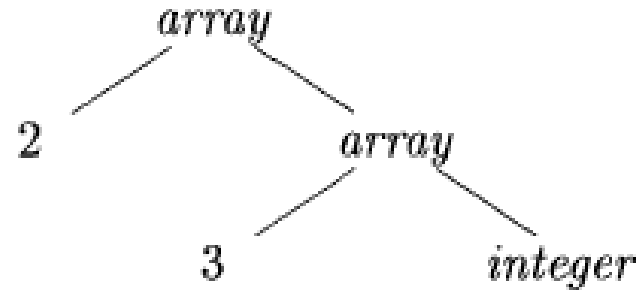
	op	arg1	arg2
0	minus	c	
1	*	b	(0)
2	minus	c	
3	*	b	(2)
4	+	(1)	(3)
5	=	a	(4)

## Indirect Triples

	op		op	arg1	arg2
35	(0)		0	minus	c
36	(1)		1	*	b (0)
37	(2)		2	minus	c
38	(3)		3	*	b (2)
39	(4)		4	+	(1) (3)
40	(5)		5	=	a (4)

# Type Expressions

Example:            `int[2][3]`  
                      `array(2,array(3,integer))`



- A basic type is a type expression
- A type name is a type expression
- A type expression can be formed by applying the array type constructor to a number and a type expression.
- A record is a data structure with named field
- A type expression can be formed by using the type constructor → for function types
- If  $s$  and  $t$  are type expressions, then their Cartesian product  $s * t$  is a type expression
- Type expressions may contain variables whose values are type expressions

# Type Equivalence

- They are the same basic type.
- They are formed by applying the same constructor to structurally equivalent types.
- One is a type name that denotes the other.

# Declarations

$$D \rightarrow T \text{ id } ; D \mid \epsilon$$
$$T \rightarrow B C \mid \text{record } \{ D \}$$
$$B \rightarrow \text{int} \mid \text{float}$$
$$C \rightarrow \epsilon \mid [ \text{num} ] C$$

# Storage Layout for Local Names

- Computing types and their widths

$$T \rightarrow \begin{matrix} B \\ C \end{matrix} \quad \{ t = B.type; w = B.width; \}$$

$$B \rightarrow \mathbf{int} \quad \{ B.type = integer; B.width = 4; \}$$

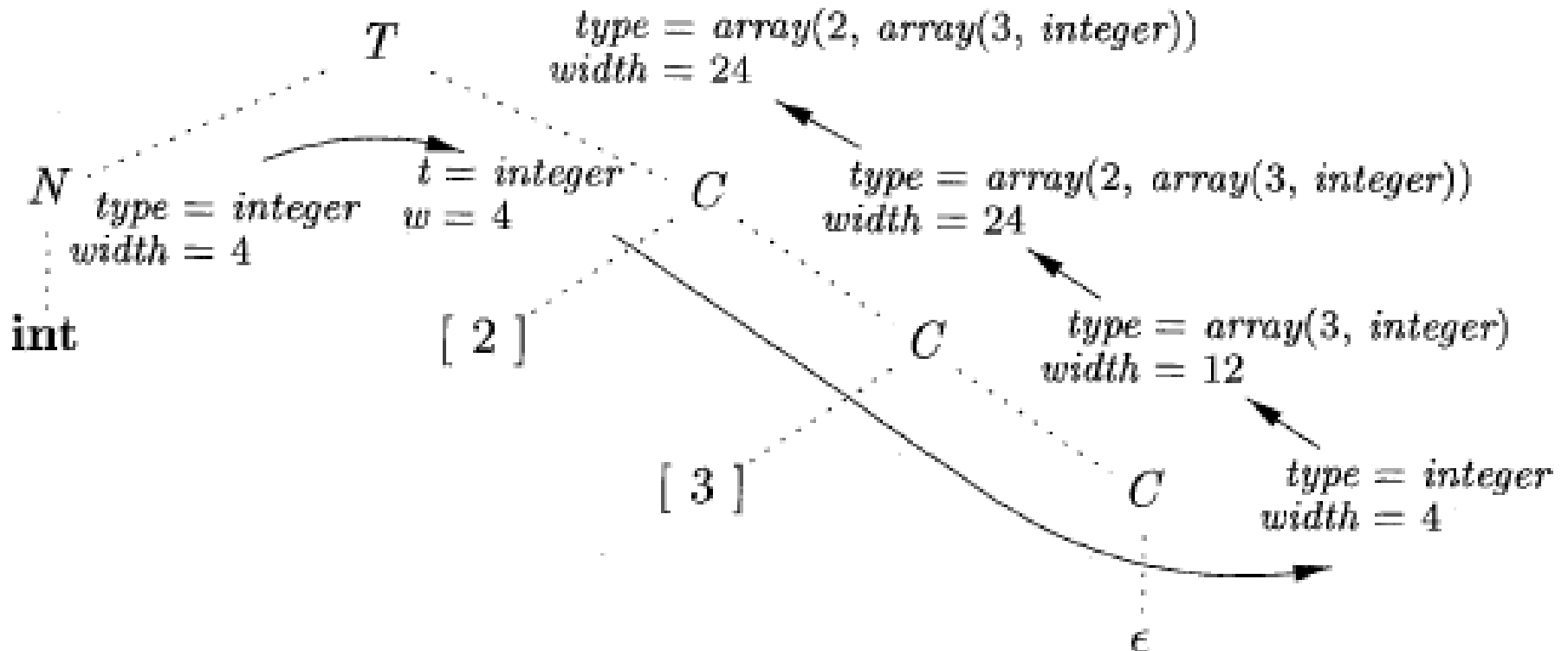
$$B \rightarrow \mathbf{float} \quad \{ B.type = float; B.width = 8; \}$$

$$C \rightarrow \epsilon \quad \{ C.type = t; C.width = w; \}$$

$$C \rightarrow [ \mathbf{num} ] C_1 \quad \{ \text{array}(\mathbf{num.value}, C_1.type); \\ C.width = \mathbf{num.value} \times C_1.width; \}$$

# Storage Layout for Local Names

- Syntax-directed translation of array types





# Sequences of Declarations

- $$P \rightarrow D \quad \{ \textit{offset} = 0; \}$$
$$D \rightarrow T \textit{id} ; \quad \{ \textit{top.put}(\textit{id.lexeme}, T.\textit{type}, \textit{offset});$$
$$\quad \quad \quad \textit{offset} = \textit{offset} + T.\textit{width}; \}$$
$$D \rightarrow D_1$$
$$D \rightarrow \epsilon$$

- Actions at the end:

- $$P \rightarrow M D$$
$$M \rightarrow \epsilon \quad \{ \textit{offset} = 0; \}$$

# Fields in Records and Classes

- ```
float x;  
record { float x; float y; } p;  
record { int tag; float x; float y; } q;
```
- $T \rightarrow \text{record } \{ \{ \text{Env.push}(top); top = \text{new Env}();$   
 $\text{Stack.push}(offset); offset = 0; \}$   
 $D \} \{ T.type = \text{record}(top); T.width = offset;$   
 $top = \text{Env.pop}(); offset = \text{Stack.pop}(); \}$

# Translation of Expressions and Statements

- We discussed how to find the types and offset of variables
- We have therefore necessary preparations to discuss about translation to intermediate code
- We also discuss the type checking

# Three-address code for expressions

| PRODUCTION                        | SEMANTIC RULES                                                                                                                |
|-----------------------------------|-------------------------------------------------------------------------------------------------------------------------------|
| $S \rightarrow \mathbf{id} = E ;$ | $S.code = E.code \parallel$<br>$gen(top.get(\mathbf{id.lexeme}) '=' E.addr)$                                                  |
| $E \rightarrow E_1 + E_2$         | $E.addr = \mathbf{new Temp}()$<br>$E.code = E_1.code \parallel E_2.code \parallel$<br>$gen(E.addr '=' E_1.addr '+' E_2.addr)$ |
| $  - E_1$                         | $E.addr = \mathbf{new Temp}()$<br>$E.code = E_1.code \parallel$<br>$gen(E.addr '=' 'minus' E_1.addr)$                         |
| $  ( E_1 )$                       | $E.addr = E_1.addr$<br>$E.code = E_1.code$                                                                                    |
| $  \mathbf{id}$                   | $E.addr = top.get(\mathbf{id.lexeme})$<br>$E.code = ''$                                                                       |

# Incremental Translation

$S \rightarrow \mathbf{id} = E ; \quad \{ \text{gen}( \text{top.get}(\mathbf{id.lexeme}) \text{'='} E.addr); \}$

$E \rightarrow E_1 + E_2 \quad \{ E.addr = \mathbf{new Temp}();$   
 $\quad \text{gen}(E.addr \text{'='} E_1.addr \text{'+'} E_2.addr); \}$

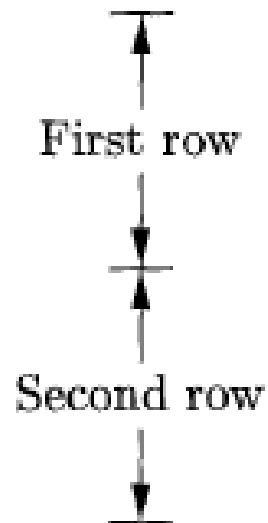
|  $- E_1 \quad \{ E.addr = \mathbf{new Temp}();$   
 $\quad \text{gen}(E.addr \text{'='} \text{'minus'} E_1.addr); \}$

|  $( E_1 ) \quad \{ E.addr = E_1.addr; \}$

|  $\mathbf{id} \quad \{ E.addr = \text{top.get}(\mathbf{id.lexeme}); \}$

# Addressing Array Elements

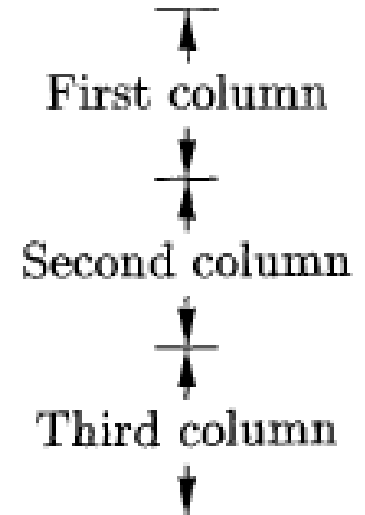
- Layouts for a two-dimensional array:



|           |
|-----------|
| $A[1, 1]$ |
| $A[1, 2]$ |
| $A[1, 3]$ |
| $A[2, 1]$ |
| $A[2, 2]$ |
| $A[2, 3]$ |

(a) Row Major

|           |
|-----------|
| $A[1, 1]$ |
| $A[2, 1]$ |
| $A[1, 2]$ |
| $A[2, 2]$ |
| $A[1, 3]$ |
| $A[2, 3]$ |



(b) Column Major

# Semantic actions for array reference

```
S → id = E ; { gen( top.get(id.lexeme) != E.addr); }  
  
  | L = E ; { gen(L.addr.base '[' L.addr ']' != E.addr); }  
  
E → E1 + E2 { E.addr = new Temp ();  
                 gen(E.addr != E1.addr '+' E2.addr); }  
  
  | id      { E.addr = top.get(id.lexeme); }  
  
  | L        { E.addr = new Temp ();  
                 gen(E.addr != L.array.base '[' L.addr ']); }  
  
L → id [ E ] { L.array = top.get(id.lexeme);  
                L.type = L.array.type.elem;  
                L.addr = new Temp ();  
                gen(L.addr != E.addr '*' L.type.width); }  
  
  | L1 [ E ] { L.array = L1.array;  
                L.type = L1.type.elem;  
                t = new Temp ();  
                L.addr = new Temp ();  
                gen(t != E.addr '*' L.type.width); }  
                gen(L.addr != L1.addr '+' t); }
```

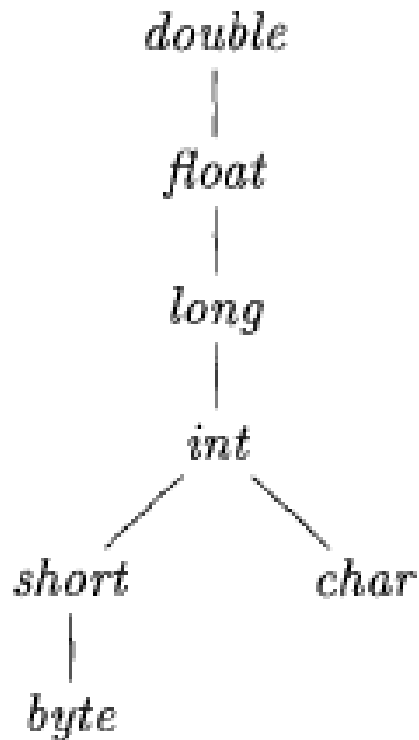
# Translation of Array References

Nonterminal  $L$  has three synthesized attributes:

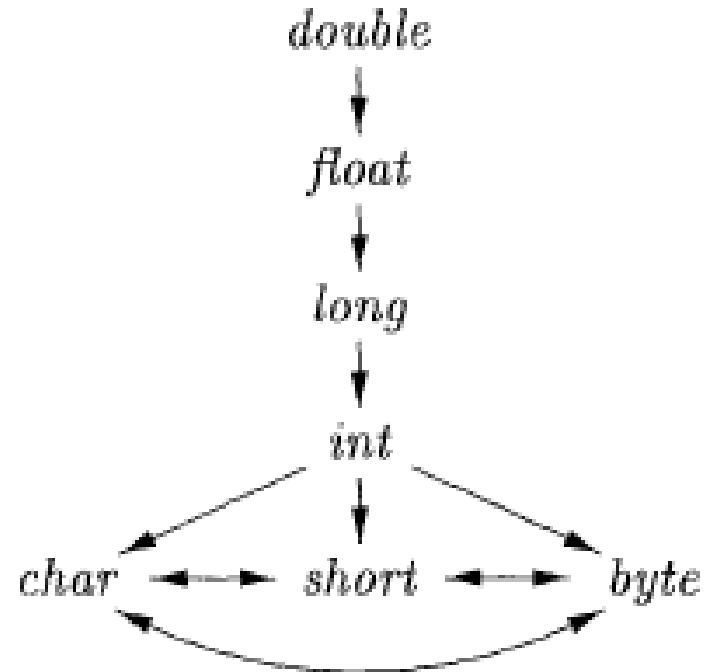
- $L.addr$
- $L.array$
- $L.type$



# Conversions between primitive types in Java



(a) Widening conversions



(b) Narrowing conversions

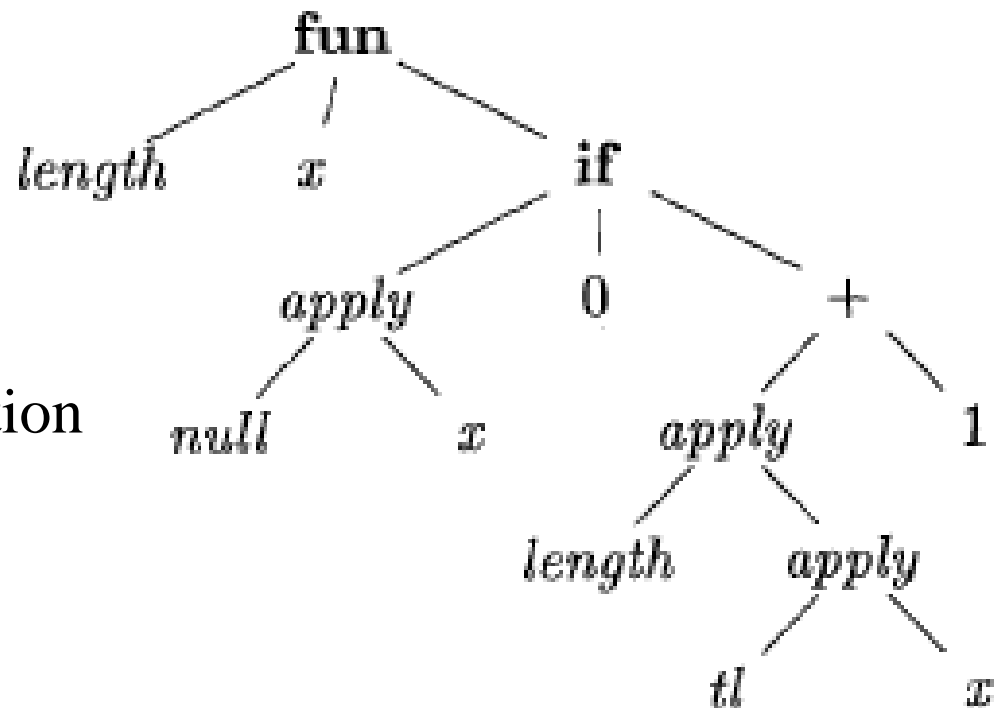
# Introducing type conversions into expression evaluation

```
E → E1 + E2 { E.type = max(E1.type, E2.type);  
                  a1 = widen(E1.addr, E1.type, E.type);  
                  a2 = widen(E2.addr, E2.type, E.type);  
                  E.addr = new Temp();  
                  gen(E.addr '=' a1 '+' a2); }
```

# Abstract syntax tree for the function definition

```
fun length(x) =  
  if null(x) then 0 else length(tl(x)+1)
```

This is a polymorphic function  
in ML language



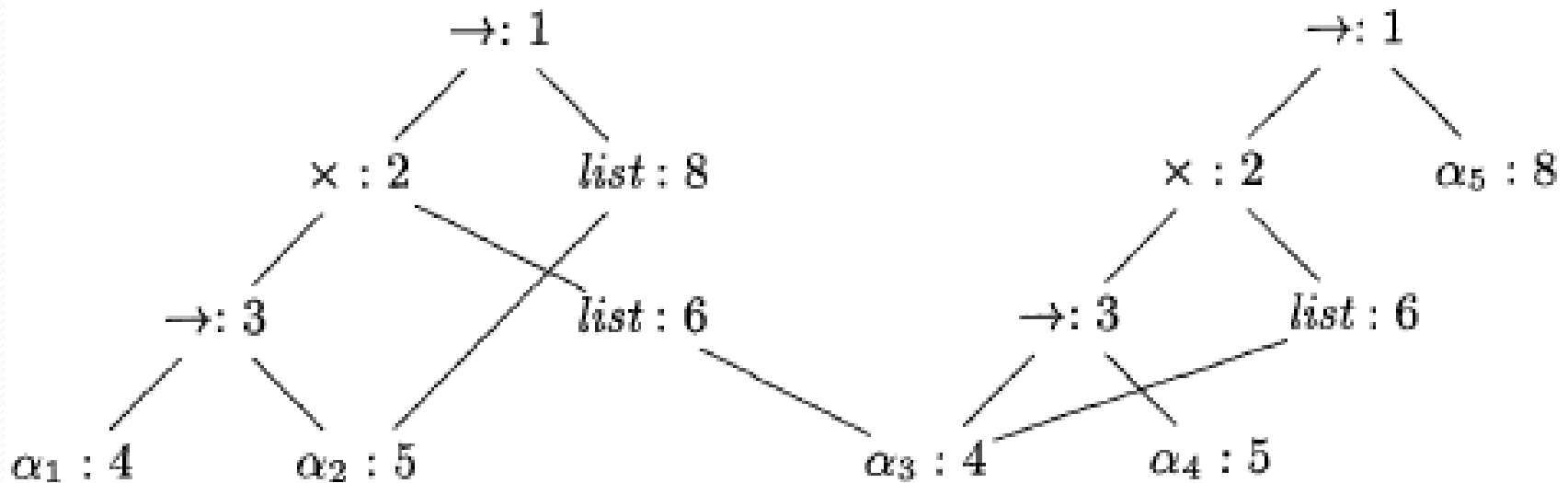
# Inferring a type for the function *length*

| LINE | EXPRESSION : TYPE                                                   | UNIFY                             |
|------|---------------------------------------------------------------------|-----------------------------------|
| 1)   | $length : \beta \rightarrow \gamma$                                 |                                   |
| 2)   | $x : \beta$                                                         |                                   |
| 3)   | $if : boolean \times \alpha_i \times \alpha_i \rightarrow \alpha_i$ |                                   |
| 4)   | $null : list(\alpha_n) \rightarrow boolean$                         |                                   |
| 5)   | $null(x) : boolean$                                                 | $list(\alpha_n) = \beta$          |
| 6)   | $0 : integer$                                                       | $\alpha_i = integer$              |
| 7)   | $+ : integer \times integer \rightarrow integer$                    |                                   |
| 8)   | $tl : list(\alpha_t) \rightarrow list(\alpha_t)$                    |                                   |
| 9)   | $tl(x) : list(\alpha_t)$                                            | $list(\alpha_t) = list(\alpha_n)$ |
| 10)  | $length(tl(x)) : \gamma$                                            | $\gamma = integer$                |
| 11)  | $1 : integer$                                                       |                                   |
| 12)  | $length(tl(x)) + 1 : integer$                                       |                                   |
| 13)  | $if( \dots ) : integer$                                             |                                   |

# Algorithm for Unification

$$((\alpha_1 \rightarrow \alpha_2) \times \text{list}(\alpha_3)) \rightarrow \text{list}(\alpha_2)$$

$$((\alpha_3 \rightarrow \alpha_4) \times \text{list}(\alpha_3)) \rightarrow \alpha_5$$



# Unification algorithm

```
boolean unify (Node m, Node n) {  
    s = find(m); t = find(n);  
    if ( s = t ) return true;  
    else if ( nodes s and t represent the same basic type ) return true;  
    else if ( s is an op-node with children s1 and s2 and  
             t is an op-node with children t1 and t2) {  
        union(s , t) ;  
        return unify(s1, t1) and unify(s2, t2);  
    }  
    else if s or t represents a variable {  
        union(s, t) ;  
        return true;  
    }  
    else return false;  
}
```

# Control Flow

boolean expressions are often used to:

- *Alter the flow of control.*
- *Compute logical values.*

# Short-Circuit Code

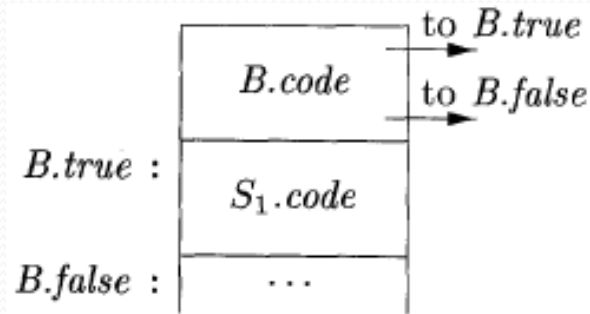
- `if ( x < 100 || x > 200 && x != y ) x = 0;`

- ```
if x < 100 goto L2
ifFalse x > 200 goto L1
ifFalse x != y goto L1
L2:  x = 0
L1:
```

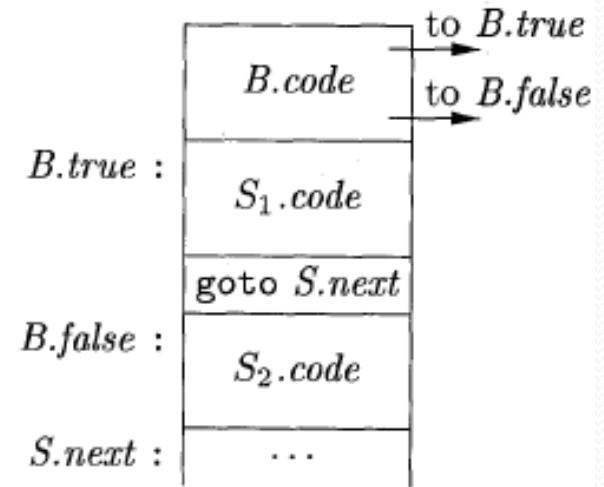


# Flow-of-Control Statements

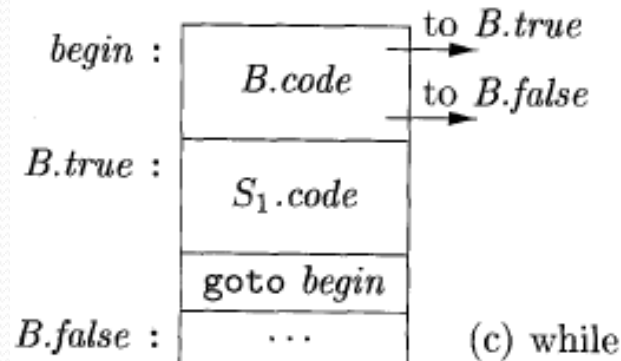
$S \rightarrow \text{if} ( B ) S_1$   
 $S \rightarrow \text{if} ( B ) S_1 \text{ else } S_2$   
 $S \rightarrow \text{while} ( B ) S_1$



(a) if



(b) if-else



(c) while

# Syntax-directed definition

PRODUCTION	SEMANTIC RULES
$P \rightarrow S$	$S.next = newlabel()$ $P.code = S.code \parallel label(S.next)$
$S \rightarrow \text{assign}$	$S.code = \text{assign}.code$
$S \rightarrow \text{if} ( B ) S_1$	$B.true = newlabel()$ $B.false = S_1.next = S.next$ $S.code = B.code \parallel label(B.true) \parallel S_1.code$
$S \rightarrow \text{if} ( B ) S_1 \text{ else } S_2$	$B.true = newlabel()$ $B.false = newlabel()$ $S_1.next = S_2.next = S.next$ $S.code = B.code$ $\quad \parallel label(B.true) \parallel S_1.code$ $\quad \parallel gen('goto' S.next)$ $\quad \parallel label(B.false) \parallel S_2.code$
$S \rightarrow \text{while} ( B ) S_1$	$begin = newlabel()$ $B.true = newlabel()$ $B.false = S.next$ $S_1.next = begin$ $S.code = label(begin) \parallel B.code$ $\quad \parallel label(B.true) \parallel S_1.code$ $\quad \parallel gen('goto' begin)$
$S \rightarrow S_1 S_2$	$S_1.next = newlabel()$ $S_2.next = S.next$ $S.code = S_1.code \parallel label(S_1.next) \parallel S_2.code$

# Generating three-address code for booleans

PRODUCTION	SEMANTIC RULES
$B \rightarrow B_1 \    \ B_2$	$B_1.true = B.true$ $B_1.false = newlabel()$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \    \ label(B_1.false) \    \ B_2.code$
$B \rightarrow B_1 \ \&\& \ B_2$	$B_1.true = newlabel()$ $B_1.false = B.false$ $B_2.true = B.true$ $B_2.false = B.false$ $B.code = B_1.code \    \ label(B_1.true) \    \ B_2.code$
$B \rightarrow ! B_1$	$B_1.true = B.false$ $B_1.false = B.true$ $B.code = B_1.code$
$B \rightarrow E_1 \ rel \ E_2$	$B.code = E_1.code \    \ E_2.code$ $\quad \    \ gen('if' \ E_1.addr \ rel \ op \ E_2.addr \ 'goto' \ B.true)$ $\quad \    \ gen('goto' \ B.false)$
$B \rightarrow \mathbf{true}$	$B.code = gen('goto' \ B.true)$
$B \rightarrow \mathbf{false}$	$B.code = gen('goto' \ B.false)$

# translation of a simple if-statement

- `if( x < 100 || x > 200 && x != y ) x = 0;`

- ```
    if x < 100 goto L2
    goto L3
L3:  if x > 200 goto L4
    goto L1
L4:  if x != y goto L2
    goto L1
L2:  x = 0
L1:
```

# Backpatching

- Previous codes for Boolean expressions insert symbolic labels for jumps
- It therefore needs a separate pass to set them to appropriate addresses
- We can use a technique named backpatching to avoid this
- We assume we save instructions into an array and labels will be indices in the array
- For nonterminal B we use two attributes B.truelist and B.falselist together with following functions:
  - makelist(i): create a new list containing only I, an index into the array of instructions
  - Merge(p1,p2): concatenates the lists pointed by p1 and p2 and returns a pointer to the concatenated list
  - Backpatch(p,i): inserts i as the target label for each of the instruction on the list pointed to by p

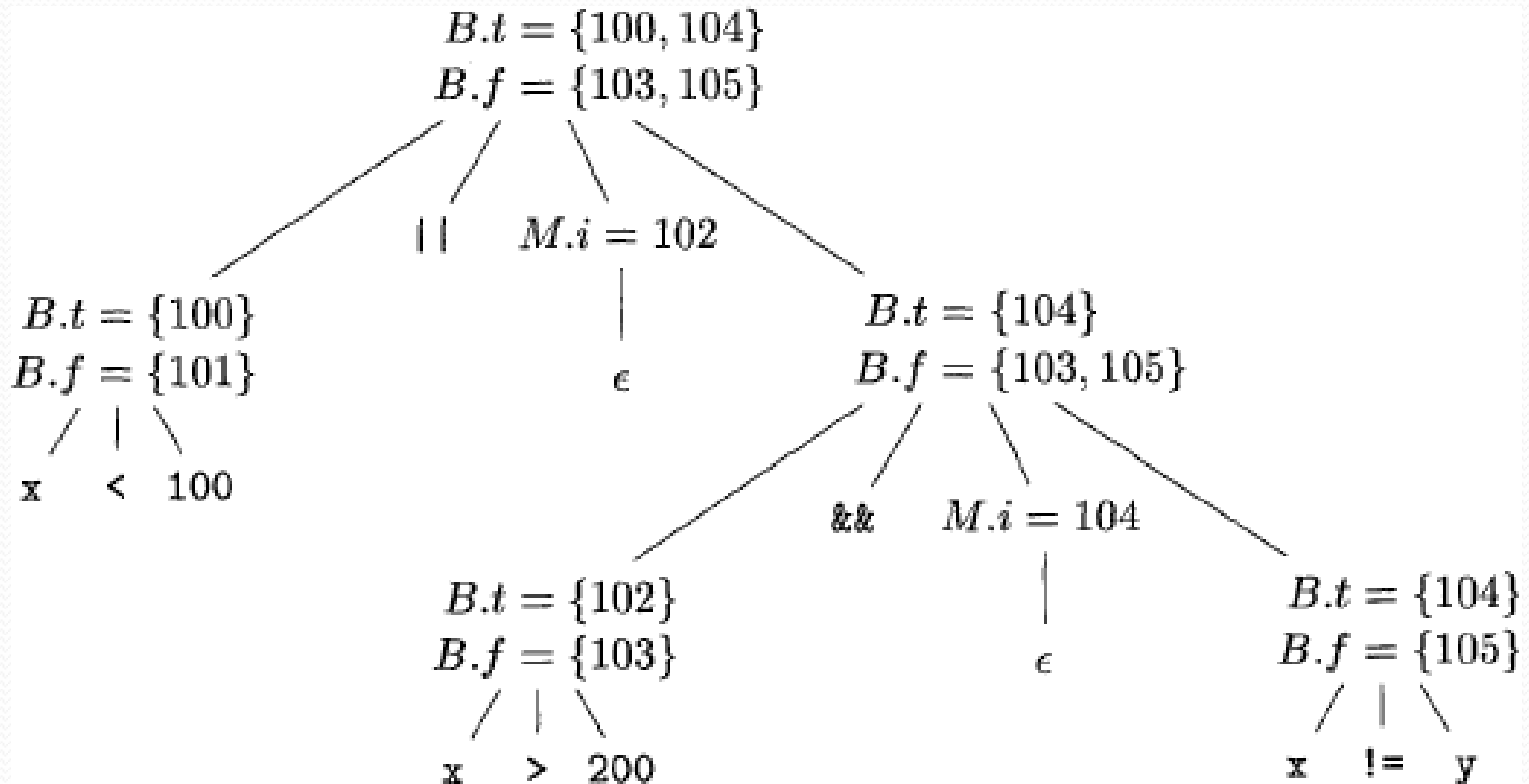
# Backpatching for Boolean Expressions

- $B \rightarrow B_1 \ || \ M \ B_2 \ | \ B_1 \ \&\& \ M \ B_2 \ | \ ! \ B_1 \ | \ ( \ B_1 \ ) \ | \ E_1 \ \text{rel} \ E_2 \ | \ \text{true} \ | \ \text{false}$   
 $M \rightarrow \epsilon$

- 1)  $B \rightarrow B_1 \ || \ M \ B_2$     { *backpatch*( $B_1$ .*false*list,  $M$ .*instr*);  
                                   $B$ .*true*list = *merge*( $B_1$ .*true*list,  $B_2$ .*true*list);  
                                   $B$ .*false*list =  $B_2$ .*false*list; }
- 2)  $B \rightarrow B_1 \ \&\& \ M \ B_2$     { *backpatch*( $B_1$ .*true*list,  $M$ .*instr*);  
                                   $B$ .*true*list =  $B_2$ .*true*list;  
                                   $B$ .*false*list = *merge*( $B_1$ .*false*list,  $B_2$ .*false*list); }
- 3)  $B \rightarrow ! \ B_1$                 {  $B$ .*true*list =  $B_1$ .*false*list;  
                                   $B$ .*false*list =  $B_1$ .*true*list; }
- 4)  $B \rightarrow ( \ B_1 \ )$             {  $B$ .*true*list =  $B_1$ .*true*list;  
                                   $B$ .*false*list =  $B_1$ .*false*list; }
- 5)  $B \rightarrow E_1 \ \text{rel} \ E_2$         {  $B$ .*true*list = *makelist*(*nextinstr*);  
                                   $B$ .*false*list = *makelist*(*nextinstr* + 1);  
                                  *emit*('if'  $E_1$ .*addr* *rel.op*  $E_2$ .*addr* 'goto -');  
                                  *emit*('goto -'); }
- 6)  $B \rightarrow \text{true}$                  {  $B$ .*true*list = *makelist*(*nextinstr*);  
                                  *emit*('goto -'); }
- 7)  $B \rightarrow \text{false}$                 {  $B$ .*false*list = *makelist*(*nextinstr*);  
                                  *emit*('goto -'); }
- 8)  $M \rightarrow \epsilon$                     {  $M$ .*instr* = *nextinstr*; }

# Backpatching for Boolean Expressions

- Annotated parse tree for  $x < 100 \ || \ x > 200 \ \&\& \ x \neq y$



# Flow-of-Control Statements

1)  $S \rightarrow \text{if}(B) M S_1 \{ \text{backpatch}(B.\text{truelist}, M.\text{instr});$   
 $S.\text{nextlist} = \text{merge}(B.\text{falselist}, S_1.\text{nextlist}); \}$

2)  $S \rightarrow \text{if}(B) M_1 S_1 N \text{ else } M_2 S_2$   
 $\{ \text{backpatch}(B.\text{truelist}, M_1.\text{instr});$   
 $\text{backpatch}(B.\text{falselist}, M_2.\text{instr});$   
 $\text{temp} = \text{merge}(S_1.\text{nextlist}, N.\text{nextlist});$   
 $S.\text{nextlist} = \text{merge}(\text{temp}, S_2.\text{nextlist}); \}$

3)  $S \rightarrow \text{while } M_1 (B) M_2 S_1$   
 $\{ \text{backpatch}(S_1.\text{nextlist}, M_1.\text{instr});$   
 $\text{backpatch}(B.\text{truelist}, M_2.\text{instr});$   
 $S.\text{nextlist} = B.\text{falselist};$   
 $\text{emit}(\text{'goto' } M_1.\text{instr}); \}$

$S \rightarrow \text{while } M_1 (B) M_2 S_1$

4)  $S \rightarrow \{ L \} \quad \{ S.\text{nextlist} = L.\text{nextlist}; \}$

5)  $S \rightarrow A ; \quad \{ S.\text{nextlist} = \text{null}; \}$

6)  $M \rightarrow \epsilon \quad \{ M.\text{instr} = \text{nextinstr}; \}$

7)  $N \rightarrow \epsilon \quad \{ N.\text{nextlist} = \text{makelist}(\text{nextinstr});$   
 $\text{emit}(\text{'goto' } -'); \}$

8)  $L \rightarrow L_1 M S \quad \{ \text{backpatch}(L_1.\text{nextlist}, M.\text{instr});$   
 $L.\text{nextlist} = S.\text{nextlist}; \}$



# Translation of a switch-statement

|  |             |                                 |                                    |
|--|-------------|---------------------------------|------------------------------------|
|  |             | code to evaluate $E$ into $t$   |                                    |
|  |             | goto test                       | code to evaluate $E$ into $t$      |
|  | $L_1$ :     | code for $S_1$                  | if $t \neq V_1$ goto $L_1$         |
|  |             | goto next                       | code for $S_1$                     |
|  | $L_2$ :     | code for $S_2$                  | goto next                          |
|  |             | goto next                       | $L_1$ :                            |
|  |             | ...                             | if $t \neq V_2$ goto $L_2$         |
|  | $L_{n-1}$ : | code for $S_{n-1}$              | code for $S_2$                     |
|  |             | goto next                       | goto next                          |
|  | $L_n$ :     | code for $S_n$                  | $L_2$ :                            |
|  |             | goto next                       | ...                                |
|  | test:       | if $t = V_1$ goto $L_1$         | $L_{n-2}$ :                        |
|  |             | if $t = V_2$ goto $L_2$         | if $t \neq V_{n-1}$ goto $L_{n-1}$ |
|  |             | ...                             | code for $S_{n-1}$                 |
|  |             | if $t = V_{n-1}$ goto $L_{n-1}$ | goto next                          |
|  |             | goto $L_n$                      | $L_{n-1}$ :                        |
|  |             |                                 | code for $S_n$                     |
|  |             |                                 | next:                              |
|  | next:       |                                 |                                    |

# Readings

- Chapter 6 of the book